

10/616,303

Exhibit B

Attorney Docket No.: 43876-144

SPECIFICATION**Related Applications**

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[0001] This application claims the benefit of priority to Provisional Application No. 60/394,665 filed July 10, 2002, and is a continuation-in-part of Patent Application No. 09/922,319, filed March 24, 2000, which is a continuation of U.S. Patent Application No. 09/382,402, filed August 24, 1999, now U.S. Patent No. 6,295,599, which claims the benefit of priority to Provisional Application No. 60/097,635 filed on August 24, 1998, and which is a continuation-in-part of U.S. Patent Application No. 09/169,963, filed October 13, 1998, now U.S. Patent No. 6,006,318, which is a continuation of U.S. Patent Application No. 08/754,827, filed November 22, 1996 now U.S. Patent No. 5,822,603, which is a divisional of U.S. Patent Application No. 08/516,036, filed August 16, 1995 now U.S. Patent No. 5,742,840, each of the above applications and/or patents are herein incorporated by reference in their entirety.

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Field of the Invention

[0002] The present invention relates to general purpose processor architectures, and particularly relates to wide operand architectures.

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REFERENCE TO A "SEQUENCE LISTING," A TABLE, OR A COMPUTER PROGRAM LISTING APPENDIX SUBMITTED ON A COMPACT DISK

[0003] This application includes an appendix, submitted herewith in duplicate on compact disks labeled as "Copy 1" and "Copy 2." The contents of the compact disks are hereby incorporated by reference in their entirety.

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BACKGROUND OF THE INVENTION

[0004] Communications products require increased computational performance to process digital signals in software on a real time basis. Increases in performance have come

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